

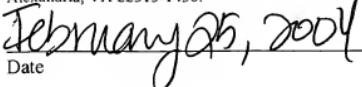
FEB 27 2004

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date

Ayesha J. Shaikh



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Joseph M. Jeddeloh	Attorney Docket No.:	501304.01
Serial No.	:	10/625,132	Group Art Unit	Not Yet Assigned
Filed	:	July 22, 2003	Examiner	Not Yet Assigned
Title	:	APPARATUS AND METHOD FOR DIRECT MEMORY ACCESS IN A HUB-BASED MEMORY SYSTEM		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. A copy of the cited non-patent literature reference, as required under 37 C.F.R. § 1.98(a)(2), is enclosed. Copies of the cited U.S. patents and U.S. patent application publications will not be submitted herewith in accordance with the waiver by the Office of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for U.S. national patent applications filed after June 30, 2003. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /ES/

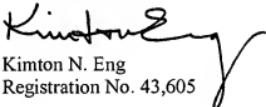
Applicants also wish to make known to the Patent and Trademark Office that an application, Serial No. 10/645,288, describing and claiming subject matter that is similar to the subject matter of this application, was filed on August 20, 2003. This related application, as well as any prior art cited therein, may be material to the examination of this application. A copy of this related application as filed is submitted herewith in accordance with 37 C.F.R. § 1.98(d).

I hereby certify that no item set forth on the attached form PTO-1449 was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge, after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



Kimton N. Eng  
Registration No. 43,605

KNE:ajs

Enclosures:

- Postcard
- Form PTO-1449
- Cited Reference (1)
- Copy of Related Application (1)

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /ES/

FORM PTO-1449 (REV.7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501304.01	APPLICATION NO. 10/625,132
<b>INFORMATION DISCLOSURE STATEMENT</b> <small>(Use several sheets if necessary)</small>				APPLICANT(S) Joseph M. Jeddelloh	
				FILING DATE July 22, 2003	GROUP ART UNIT Not Yet Assigned

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA	5,818,844	10/06/98	Singh et al.	370	463	
AB	6,272,609 B1	08/07/01	Jeddelloh	711	169	
AC	6,477,592 B1	11/05/02	Chen et al.	710	52	
AD	6,523,092 B1	02/18/03	Fanning	711	134	
AE	6,523,093 B1	02/18/03	Bogin et al.	711	137	
AF	6,622,227 B2	09/16/03	Zumkehr et al.	711	167	
AG	6,631,440 B2	10/07/03	Jenne et al.	711	105	
AH	2002/0144064 A1	10/03/02	Fanning	711	144	
AI	2003/0005223 A1	01/02/03	Coulson et al.	711	118	
AJ	2003/0229770 A1	12/11/03	Jeddelloh	711	213	
AK						
AL						
AM						
AN						

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
AO							
AP							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AQ	Intel, "Intel 840 Chipset: 82840 Memory Controller Hub (MCH)", Datasheet, October 1999, pp. 1-178.
AR	

## EXAMINER

/Eron Sorrell/

## DATE CONSIDERED

04/08/2008

\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).